ELECTRONICS ENGINEERING LAB

S.NO.	NAME OF THE EXPERIMENT	NAME OF THE APPARATUS/EQUIPMENT	MAKE	Quantity
1	V-I characteristics of PN Junction diode A) Forward bias B) Reverse bias	PN – Diode Trainer kit	**M/s.** Scientific Enterprises, Hyderabad	1
2	V – I characteristics of Zener Diode and its application as voltage Regulator	Zener Diode Trainer kit		1
3	Implementation of half wave and full wave rectifiers	PN – Diode Trainer kit		1
4	Input & Output characteristics of BJT in CE and CB configurations	CE & CB Configuration, (i/p & O/p characteristics) kit		1
5	Frequency response of CE amplifier	CE amplifier kit		1
6	Simulation of RC coupled amplifier with the design supplied	RC coupled Amplifier		1
7	Verification of Truth Table of AND, OR, NOT, NAND, NOR, Ex-OR, Ex-NOR gates using ICs.	Universal digital IC Trainer kit		1
8	Verification of Truth Tables of S-R, J-K& D flip flops using respective ICs	S-R, D, J-k, Flip-flop Trainer kit		1
		TOTAL COST:**63,750**		